

# ***MEng (Hons) Electronic Engineering***

## Programme Specification



<b>1. Programme title</b>	MEng Hons Electronic Engineering
<b>2. Awarding institution</b>	Middlesex University
<b>3. Teaching institution</b>	Middlesex University
<b>4. Details of accreditation by professional/statutory/regulatory body</b>	
<b>5. Final qualification</b>	Master of Engineering (Hons)
<b>6. Year of validation</b>	2016/17
<b>Year of amendment</b>	
<b>7. Language of study</b>	English
<b>8. Mode of study</b>	FT / TKS

### **9. Criteria for admission to the programme**

Admission to the MEng (Hons) Electronic Engineering programme will require 280 UCAS tariff points normally including a minimum of 200 points from at least two science or numerate based subjects.

In addition Middlesex University general entry requirements apply as outlined in the university's regulation B2. Applicants whose first language is not English are required to achieve 6.0 in IELTS overall (with a minimum of 5.5 in each component) or an equivalent qualification recognised by Middlesex University. The equivalence of qualifications from outside UK will be determined according to NARIC guidelines.

We welcome applicants with a wide variety of educational experience including: A/AS levels, AVCE, BTEC National Diploma, Access Certificates, Scottish Highers, Irish Leaving Certificates (Higher Level), International Baccalaureate and a large number of equivalent home and overseas qualifications. Application from mature applicants with suitable life skills and experiences are also welcomed.

## 10. Aims of the programme

This programme aims to produce professional and competent Electronic Engineers capable of playing an active role in formulating, meeting the challenges and opportunities arising in contemporary industrial and commercial practice.

Students will develop core electronics design capabilities, which are developed and enhanced progressively through the course.

This programme explores the principles underlying the design and implementation of up-to-date analogue and digital systems needed in a variety of problem domains and provides the opportunity of realising such systems.

## 11. Programme outcomes

### A. Knowledge and understanding

On completion of this programme the successful student will have:

1. Comprehensive knowledge and understanding of Scientific principles and related engineering disciplines to enable the modelling and analyse complex engineering systems, processes and products and collect and analyse data and draw conclusions for the innovative solution of unfamiliar or novel engineering design problems using future developments and technologies.
2. Extensive knowledge and understanding of concepts, principles and theories of the design process and an appreciation of their limitations.
3. Knowledge and understanding of the application of a systems approach to solving complex engineering problems within the context of Electronic Engineering.
4. Comprehensive knowledge and understanding of analytical techniques and engineering science relevant to Design Engineering within the context of Electronic Engineering.
5. Detailed understanding of the issues involved in systems engineering and the range of approaches used in industry to manage the resulting complexity.
6. Extensive knowledge and understanding of developing new technologies and applications relevant to Electronic Engineering.
7. Extensive knowledge and understanding of current commercial, management and business practices and their limitations relating to engineering and to new product development.
8. Knowledge and understanding of professional and ethical responsibilities of engineers.
9. Comprehensive knowledge and understanding of the role and limitations of common ICT tools and ability to specify requirements for computer-based engineering design tools to solve unfamiliar problems.
10. Extensive knowledge and understanding of a wide range of engineering materials and components.

### Teaching/learning methods

Students gain knowledge and understanding takes place through a combination of lectures, seminars, exercise classes, design build and test projects, forensic deconstruction, CAE and IT workshops, laboratory classes, industrial visits, group and individual project work, experimenting, constructing, analysing, assessing and discussing and self-study.

### Assessment methods

Students' knowledge and understanding is assessed by technical reports, coursework assignments, essays, presentations, and practical in-class tests.

<p><b>B. Cognitive (thinking) skills</b></p> <p>On completion of this programme the successful student will be able to:</p> <ol style="list-style-type: none"> <li>1. Critically analyse and solve engineering problems using appropriate techniques and through critical thinking.</li> <li>2. Model and critically analyse relevant engineering systems.</li> <li>3. Fully engage with the design process.</li> <li>4. Select, justify and apply appropriate computer based methods for solving design engineering problems.</li> <li>5. Fully evaluate external influences on the design process.</li> <li>6. Design creative and innovative systems, components or processes.</li> </ol>	<p><b>Teaching/learning methods</b></p> <p>Students learn cognitive skills through design projects, problem solving activities and through report writing.</p> <p><b>Assessment methods</b></p> <p>Students' cognitive skills are assessed by the products and systems design, with particular reference to their engagement with the design process and by coursework comprised of reports and essays.</p>
<p><b>C. Practical skills</b></p> <p>On completion of the programme the successful student will be able to:</p> <ol style="list-style-type: none"> <li>1. Plan, manage and undertake a design project, team or individual, including establishing user needs and technical specification, concept generation and evaluation, embodiment and detail design work, verification and review.</li> <li>2. Critically evaluate technical risk with an awareness of the limitations of possible solutions.</li> <li>3. Use relevant laboratory and test equipment.</li> <li>4. Create CAD models and make physical models and prototypes.</li> <li>5. Interface different technologies to develop integrated systems.</li> <li>6. Apply engineering design techniques, taking into account a selection of commercial and industrial constraints.</li> <li>7. Effectively apply understanding of concepts from a range of fields including those outside engineering to engineering design projects.</li> </ol>	<p><b>Teaching/learning methods</b></p> <p>Students learn practical skills through design projects, specific skills inputs and set exercises.</p> <p><b>Assessment methods</b></p> <p>Students' practical skills are assessed by individual and group projects, lab reports, coursework assignments and practical tests.</p>
<p><b>D. Graduate skills</b></p> <p>On completion of this programme the successful student will be able to:</p> <ol style="list-style-type: none"> <li>1. Communicate effectively in writing, verbally, graphically and through presentations to groups.</li> <li>2. Critically apply mathematical methods, computer models, and a scientific approach to solving problems in engineering design.</li> <li>3. Demonstrate leadership skills and the ability to work effectively as a member of a team.</li> <li>4. Write computer programmes and use CAE software and general IT tools and provide technical documentation.</li> <li>5. Learn independently and to adopt a critical approach in investigation.</li> <li>6. Use technical literature and other information sources effectively including electronic media.</li> </ol>	<p><b>Teaching/learning methods</b></p> <p>Students acquire graduate skills through design projects, competitions, problem solving activities, presentations, and through report writing.</p> <p><b>Assessment methods</b></p> <p>Students' graduate skills are assessed by coursework assignments including design reports, laboratory reports, other written reports, problems sheets, case studies, software programs, industrial placement, group and individual project reports.</p>

## 12. Programme structure (levels, modules, credits and progression requirements)

### 12. 1 Overall structure of the programme

<b>Year 1</b>				
AY	<b>PDE1400</b> Design Engineering Projects 1 [30]	<b>PDE1410</b> Physical Computing: Electronics [30]	<b>PDE1420</b> Physical Computing: Programming [30]	<b>PDE1430</b> Formal Systems[30]
<b>Year 2</b>				
AY	<b>PDE2400</b> Design Engineering Projects 2 [30]	<b>PDE2410</b> Engineering in Context [30]	<b>PDE2420</b> Control Systems [30]	<b>PDE2431</b> Analogue and Digital Systems [30]
<b>Year 3</b>	<b>PDE3250</b> - Thick Sandwich Placement (compulsory for TKSW only)			
<b>Year 3/4</b>				
<b>Term 1</b>	<b>PDE3411</b> System-on-a-chip design [30]		<b>PDE3420</b> Systems Design and Validation [30]	
<b>Term 2</b>	<b>PDE3400</b> Design Engineering Major Project [60]			
<b>Year 4/5</b>				
<b>Term 1</b>	Optional Module		Optional Module	
<b>Term 2</b>	PDE4400 Team Project [60]			

#### *Year 4/5 options*

*Students are required to take 2 from*

**PDE4853** Design Engineering Dissertation [30]

**PDE4880** Mobile Communications [30]

**PDE4802** Advanced topics in Electronic Engineering [30]

<b>12.2 Levels and modules</b>		
Starting in academic year 2010/11 the University is changing the way it references modules to state the level of study in which these are delivered. This is to comply with the national Framework for Higher Education Qualifications. This implementation will be a gradual process whilst records are updated. Therefore the old coding is bracketed below.		
Level 4 (1)		
COMPULSORY	OPTIONAL	PROGRESSION REQUIREMENTS
<p>Students must take all of the following: Students must take all of the following:</p> <p>PDE1400 Design Engineering Projects 1 [30]</p> <p>PDE1410 Physical Computing: Electronics [30]</p> <p>PDE1420 Physical Computing: Programming [30]</p> <p>PDE1430 Formal Systems [30]</p>	N/A	Students must pass all level 4 modules to progress.
Level 5 (2)		
COMPULSORY	OPTIONAL	PROGRESSION REQUIREMENTS
<p>Students must take all of the following:</p> <p>PDE2400 Design Engineering Projects 2 [30]</p> <p>PDE2410 Engineering in Context [30]</p> <p>PDE2420 Control Systems [30]</p> <p>PDE2431 Analogue and Digital Systems [30]</p>	N/A:	<p>TKSW -To progress on to a placement year students must pass all modules at level 5.</p> <p>FT/PT – To progress onto level 6 on the MEng students must achieve a grade 8 or better in all modules.</p>

Level 6 (3) TKS mode only		
COMPULSORY	OPTIONAL	PROGRESSION REQUIREMENTS
TKSW mode only Students must take PDE3250 Thick Sandwich Placement (120 credits – for Diploma of Industrial Studies.)	N/A	
Level 6 (3)		
COMPULSORY	OPTIONAL	PROGRESSION REQUIREMENTS
Students must take all of the following:  PDE3411 System-on-a-chip design [30]  PDE3420 Systems Design and Validation [30]  PDE3400 Design Engineering Major project [60]		Student must pass ALL modules and achieve at least a 2.1 overall to progress to level 7.
Level 7 (3)		
COMPULSORY	OPTIONAL	PROGRESSION REQUIREMENTS
PDE4400 Team Project [60]	Students must also choose at least 2 from the following: PDE4853 Design Engineering Dissertation [30]  PDE4880 Mobile Communications [30]  PDE4802 Advanced topics in Electronic Engineering [30]	

### 12.3 Non-compensatable modules (note statement in 12.2 regarding FHEQ levels)

Module level	Module code
6	PDE3400
7	PDE4400

### 13. Curriculum map

See attached.

### 14. Information about assessment regulations

Please refer to the University Regulations.

<b>15. Placement opportunities, requirements and support (if applicable)</b>	
<p>Students on the TKS mode take a placement (36 to 48 weeks) at the end of year 2. A dedicated Employability Advisor helps in the search for an appropriate employer and provides students with appropriate Placement. They also provide students with appropriate guidance and support in preparation for, during and after placement. The placement forms the basis for an assessed report based on the organisation. At the start of the placement students are allocated an individual supervisor who provides support and advice for the duration of the project.</p> <p>Students following a TKS placement year are supported through the process of securing a placement, which includes the legal and QAA requirements for placement learning, via tutorial support and the University Placement office.</p>	
<b>16. Future careers (if applicable)</b>	
<p>As a MEng Electronic Engineering graduate you will have excellent career prospects; the range of potential employers will be vast across the private, public and not-for-profit sectors</p> <p>To support students in this activity during their students are encouraged to develop a commercial approach to design engineering via supported live projects with industrial partners and industrial placements. They undertake contextual studies into the nature and contexts of the profession. They interact with a variety of guest lecturers with professional backgrounds. They are supported in developing their exit portfolio, a CV and a career entry plan.</p> <p>Through these experiences they come to understand design in a commercial context, the nature of the design industries and to plan for their own career entry and development.</p>	
<b>17. Particular support for learning (if applicable)</b>	
<p>Meeting the learning outcomes of this programme requires active participation in the subject and all practical sessions. Supporting this level of active participation is achieved via regular contact with academic staff, productive and informed support from technical staff, supports provided by Graduate Academic Assistants (GAAs), Student Learning Assistants (SLAs) and the use of online learning materials where appropriate.</p> <p>The subject provides extensive studio, laboratory and workshop facilities where students can engage with their coursework assignments in a supported and productive environment. These areas are shared with other subjects and programmes.</p>	
<b>18. JACS code (or other relevant coding system)</b>	H150 – Engineering Design
<b>19. Relevant QAA subject benchmark group(s)</b>	Engineering
<b>20. Reference points</b>	
<p>The following reference points were used in designing the programme:</p> <ul style="list-style-type: none"> <li>• QAA Engineering subject benchmark statement (2015)</li> <li>• QAA Framework for Higher Education Qualifications in England, Wales and Northern Ireland;</li> <li>• Middlesex University Regulations;</li> <li>• Middlesex University Learning and Quality Enhancement Handbook;</li> <li>• UK Standard for Professional Engineering Competence;</li> <li>• Chartered Engineer and Incorporated Engineer Standard, Engineering Council UK, 2014;</li> <li>• The Accreditation of Higher Education Programmes, Engineering Council UK, 2014;</li> <li>• IED Engineering Design Specific Learning Outcomes for EC(UK) Accredited Degree Programmes.</li> </ul>	
<b>21. Other information</b>	
<p>Please note programme specifications provide a concise summary of the main features of the programme and the learning outcomes that a typical student might reasonably be expected to achieve if s/he takes full advantage of the learning opportunities that are provided. More detailed information about the programme can be found in the rest of your programme handbook and the university regulations.</p>	

## Curriculum map for *MEng (Hons) Electronic Engineering*

This section shows the highest level at which programme outcomes are to be achieved by all graduates, and maps programme learning outcomes against the modules in which they are assessed.

### Programme learning outcomes

Knowledge and understanding		Practical skills	
A1	Comprehensive knowledge and understanding of Scientific principles and related engineering disciplines to enable the modelling and analyse complex engineering systems, processes and products and collect and analyse data and draw conclusions for the innovative solution of unfamiliar or novel engineering design problems using future developments and technologies.	C1	Plan, manage and undertake a design project, team or individual, including establishing user needs and technical specification, concept generation and evaluation, embodiment and detail design work, verification and review.
A2	Extensive knowledge and understanding of concepts, principles and theories of the design process and an appreciation of their limitations.	C2	Critically evaluate technical risk with an awareness of the limitations of possible solutions.
A3	Knowledge and understanding of the application of a systems approach to solving complex engineering problems within the context of Electronic Engineering.	C3	Use relevant laboratory and test equipment.
A4	Comprehensive knowledge and understanding of analytical techniques and engineering science relevant to Design Engineering within the context of Electronic Engineering.	C4	Create CAD models and make physical models and prototypes.
A5	Detailed understanding of the issues involved in systems engineering and the range of approaches used in industry to manage the resulting complexity.	C5	Interface different technologies to develop integrated systems.
A6	Extensive knowledge and understanding of developing new technologies and applications relevant to Electronic Engineering.	C6	Apply engineering design techniques, taking into account of a selection of commercial and industrial constraints.
A7	Extensive knowledge and understanding of current commercial, management and business practices and their limitations relating to engineering and to new product development.	C7	Effectively apply understanding of concepts from a range of fields including those outside engineering to engineering design projects.
A8	Knowledge and understanding of professional and ethical responsibilities of engineers.	C8	
A9	Comprehensive knowledge and understanding of the role and limitations of common ICT tools and ability to specify requirements for computer-based engineering design tools to solve unfamiliar problems.		
A10	Extensive knowledge and understanding of a wide range of engineering materials and components		



Cognitive skills		Graduate Skills	
B1	Critically analyse and solve engineering problems using appropriate techniques and through critical thinking.	D1	Communicate effectively in writing, verbally, graphically and through presentations to groups.
B2	Model and critically analyse relevant engineering systems.	D2	Critically apply mathematical methods, computer models, and a scientific approach to solving problems in engineering design.
B3	Fully engage with the design process.	D3	Demonstrate leadership skills and the ability to work effectively as a member of a team.
B4	Select, justify and apply appropriate computer based methods for solving design engineering problems.	D4	Write computer programmes and use CAE software and general IT tools and provide technical documentation.
B5	Fully evaluate external influences on the design process.	D5	Learn independently and to adopt a critical approach in investigation.
B6	Design creative and innovative systems, components or processes.	D6	Use technical literature and other information sources effectively including electronic media

Programme outcomes																												
A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	B1	B2	B3	B4	B5	B6	C1	C2	C3	C4	C5	C6	C7	D1	D2	D3	D4	D5	D6
Highest Level Achieved																												
7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7

Module Title	Module Code by Level	Programme outcomes																												
		A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	B1	B2	B3	B4	B5	B6	C1	C2	C3	C4	C5	C6	C7	D1	D2	D3	D4	D5	D6
Design Engineering Projects 1	PDE1400							Y	Y			Y	Y	Y	Y		Y	Y		Y	Y			Y	Y	Y	Y	Y	Y	Y
Physical Computing: Electronics	PDE1410				Y							Y	Y							Y	Y					Y			Y	
Physical Computing: Programming	PDE1420				Y					Y		Y			Y					Y		Y			Y	Y	Y	Y	Y	
Formal Systems	PDE1430				Y							Y	Y				Y									Y			Y	
Design Engineering Projects 2	PDE2400		Y	Y				Y	Y	Y	Y	Y	Y	Y	Y	Y	Y				Y	Y	Y	Y	Y	Y	Y	Y	Y	Y
Engineering in Context	PDE2410		Y			Y		Y	Y						Y					Y			Y	Y	Y		Y		Y	Y
Control Systems	PDE2420	Y			Y	Y				Y		Y	Y		Y		Y	Y		Y		Y			Y	Y		Y		Y
Analogue and Digital Systems	PDE 2431	Y	Y	Y	Y	Y	Y			Y		Y	Y		Y		Y			Y	Y				Y		Y	Y	Y	Y
Industrial Placement	PDE3250					Y		Y	Y			Y	Y	Y				Y					Y			Y	Y	Y		Y
System-on-a-chip design	PDE3411		Y	Y			Y			Y		Y	Y		Y		Y	Y		Y		Y			Y	Y		Y	Y	Y
Systems Design and Validation	PDE3420	Y	Y	Y	Y	Y				Y			Y				Y	Y	Y							Y	Y			
Design Engineering Major project	PDE3400	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y
Team Project	PDE4400	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y
Design Engineering Dissertation	PDE4853		Y		Y		Y	Y	Y									Y	Y				Y			Y			Y	Y
Mobile Communications	PDE4880	Y		Y	Y	Y				Y			Y		Y		Y			Y	Y	Y			Y	Y				
Advanced topics in Electronic Engineering	PDE4802	Y	Y		Y		Y	Y	Y			Y			Y	Y		Y	Y				Y	Y	Y	Y		Y	Y	Y